

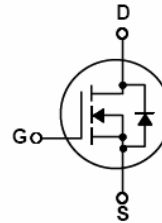
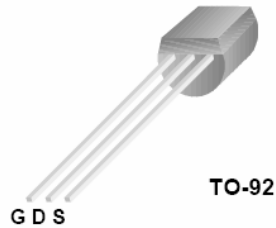
#### 1 Description

These N-Channel enhancement mode power field effect transistors are produced using planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

#### 2 Features

- 600V / 0.35A
- $R_{DS(on)} = 14\Omega$ (typ)  $V_{GS} = 10V$ ,  $I_D = 0.21A$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability..



#### 3 Absolute Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted

Symbol	Parameter	apQ0CSN60AJ	Units
$V_{DSS}$	Drain-Source Voltage	600	V
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ C$ )	0.35	A
$I_{DM}$	Drain Current – Pulsed ①	1.4	A
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy ②	33	mJ
$I_{AR}$	Avalanche Current	0.35	A
$E_{AR}$	Repetitive Avalanche Energy	0.35	mJ
dv/dt	Peak Diode Recovery dv/dt ③	1	V/ns
$P_D$	Power Dissipation ( $T_C = 25^\circ C$ ) -De-rate above $25^\circ C$	2.5	W
		3	W/ $^\circ C$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ C$
$T_L$	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ C$

\* note :

① Repetitive Rating: Pulse width limited by maximum junction temperature.

②  $V_{DD} = 50V$ , starting  $T_J = 25^\circ C$ ,  $L = 60mH$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 1.1A$

③  $I_{SD} \leq 0.35A$ ,  $di/dt \leq 200A/\mu s$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 150^\circ C$ .



# DEVICE SPECIFICATION

apQ0CSN60AJ

600V/0.35A N-Channel MOSFET

## 4 Thermal Characteristics

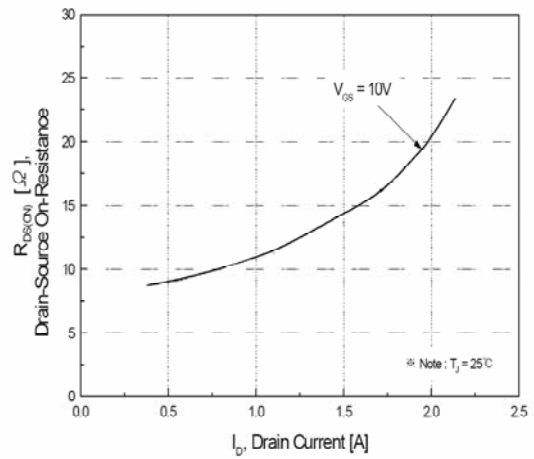
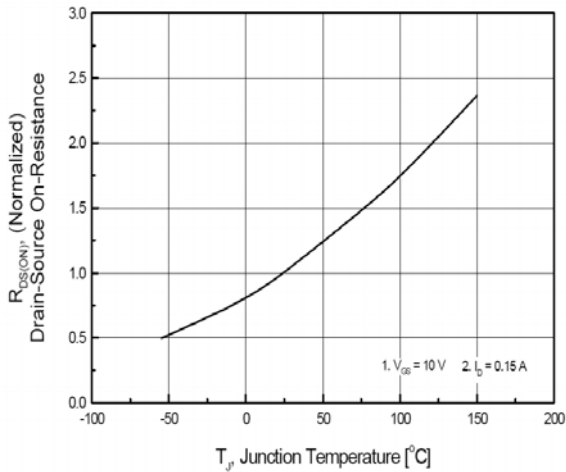
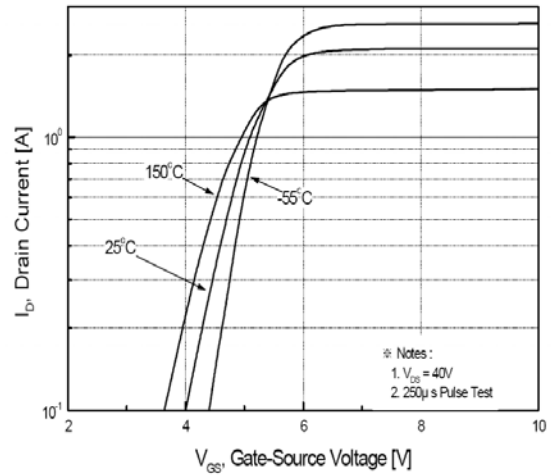
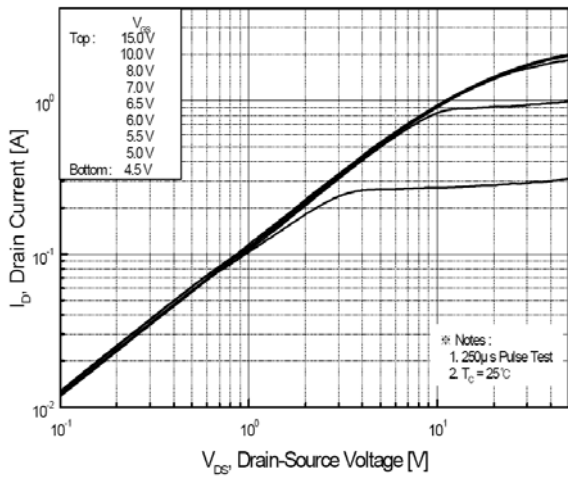
Symbol	Parameter	apQ0CSN60AJ	Units
$R_{\theta JL}$	Thermal Resistance, Junction-to-Lead	50	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	140	°C/W

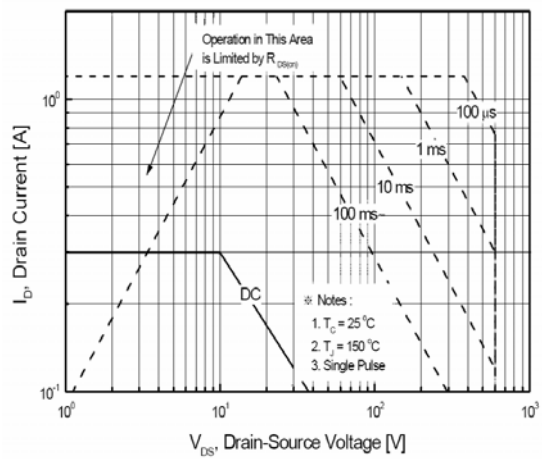
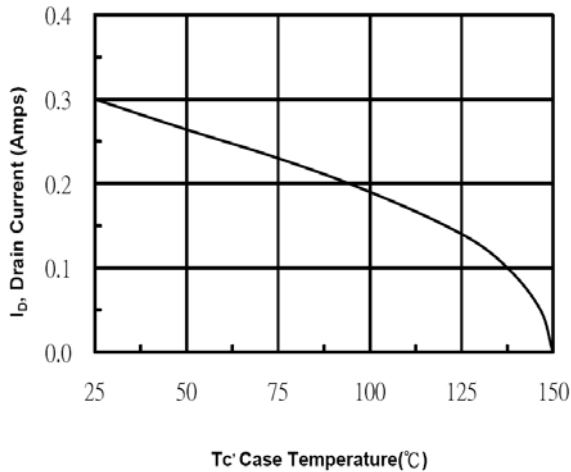
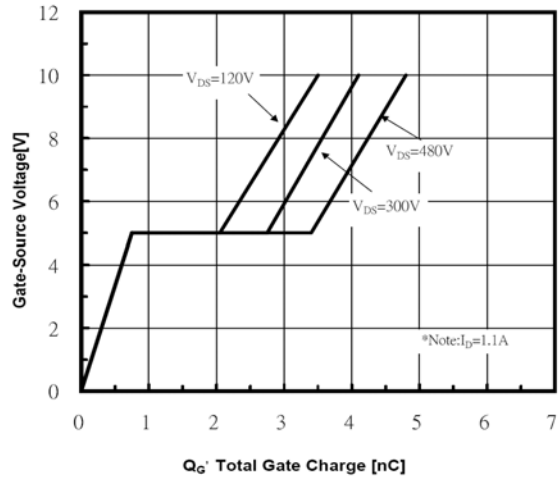
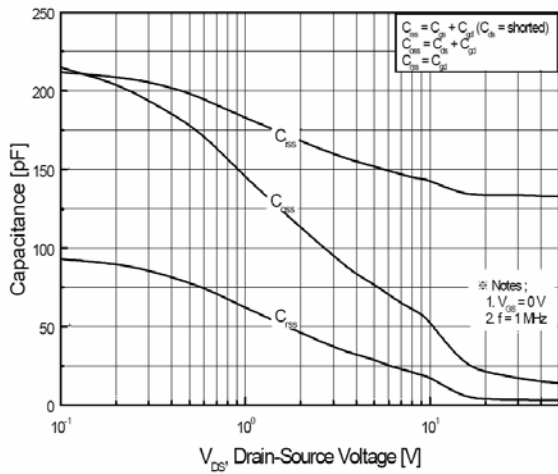
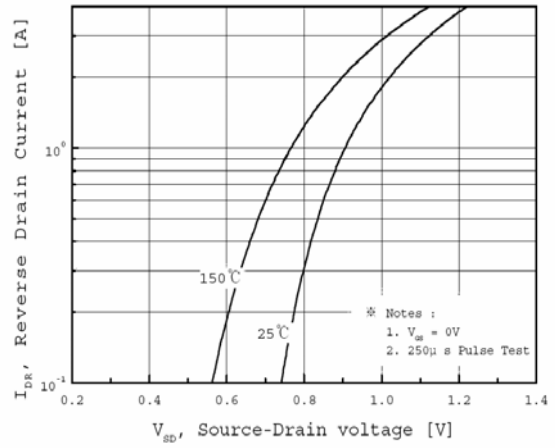
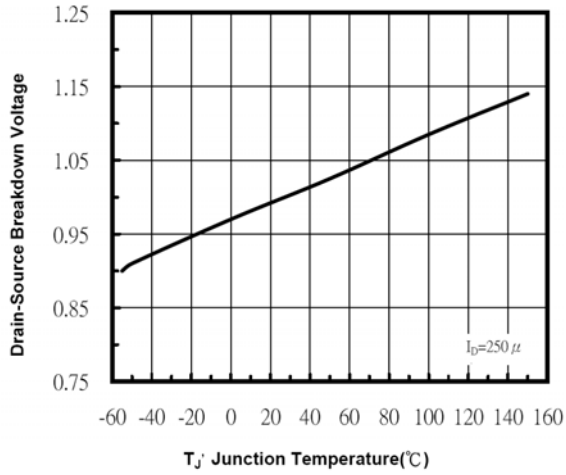
## 5 Electrical Characteristics $T_c = 25^\circ\text{C}$ unless otherwise noted

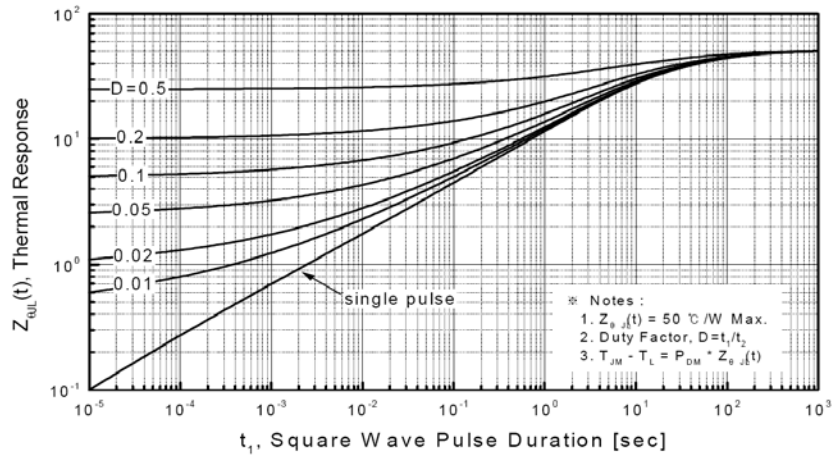
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	600	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\ \mu\text{A}$ , Referenced to $25^\circ\text{C}$	--	0.6	--	V/°C
$I_{DSS}$	Gate to Source leakage current	$V_{DS} = 600\text{ V}, V_{GS} = 0\text{ V}$	--	--	20	$\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
$I_{GSSR}$	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 0.4\text{ A}$ ④	--	14	15	$\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 15\text{ V}, I_D = 0.4\text{ A}$ ①	--	--	10	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V}, f = 1.0\text{ MHz}$	--	130	--	pF
$C_{oss}$	Output Capacitance		--	19	--	pF
$C_{rss}$	Reverse Transfer Capacitance		--	3.4	--	pF
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 300\text{ V}, I_D = 0.4\text{ A}, R_G = 10\ \Omega, R_D = 300\ \Omega, V_{GS} = 10\text{ V}$ ④	--	7.6	--	ns
$t_r$	Turn-On Rise Time		--	20	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	12	--	ns
$t_f$	Turn-Off Fall Time		--	26	--	ns
$Q_g$	Total Gate Charge	$V_{DS} = 300\text{ V}, I_D = 0.4\text{ A}, V_{GS} = 10\text{ V}$ ④	--	5.77	--	nC
$Q_{gs}$	Gate-Source Charge		--	0.65	--	nC
$Q_{gd}$	Gate-Drain Charge		--	2.72	--	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current		--	--	0.3	A
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current		--	--	1.2	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 0.5\text{ A}$	--	--	1.4	V
$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0\text{ V}, I_F = 0.4\text{ A}, di_F/dt = 100\text{ A}/\mu\text{s}$ ④	--	190	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	0.53	--	$\mu\text{C}$

**Notes:**

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ②  $V_{DD}=50V$ , starting  $T_J=25^\circ C$ ,  $L=60mH$ ,  $R_G=25\Omega$ ,  $I_{AS}=1.1A$
- ③  $I_{SD} \leq 0.35A$ ,  $di/dt \leq 200A/\mu s$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 150^\circ C$
- ④ Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ .

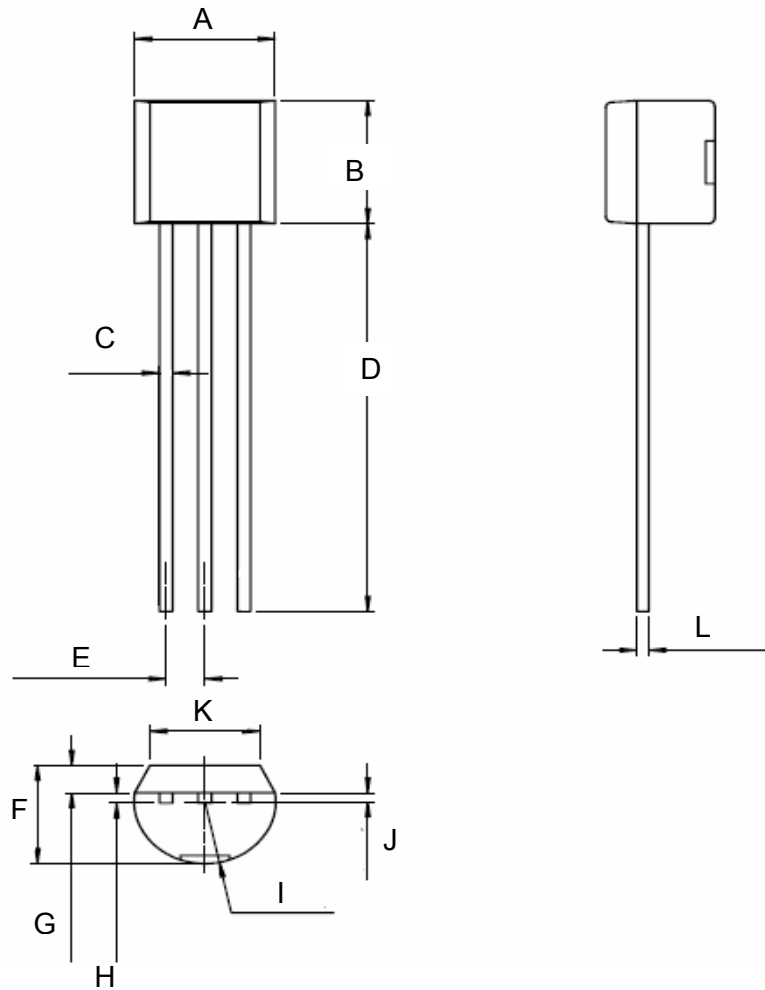






#### 6 Package Dimensions

TO-92



TO-92 DIMENSION							
DIM	MILLIMETERS						
	MIN	MAX	TYP.	DIM	MIN	MAX	TYP
A	4.43	4.83	4.63	G	0.92	1.12	1.02
B	4.38	4.78	4.58	H	0.33	0.48	0.41
C	0.36	0.56	0.46	I	$\phi$ 2.29		
D	14.07	14.87	14.47	J	0.25		
E	1.07	1.47	1.27	K	3.4	3.8	3.6
F	--	3.86	--	L	0.33	0.48	0.41



## DEVICE SPECIFICATION

apQ0CSN60AJ

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600V/0.35A N-Channel MOSFET

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### Note

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