

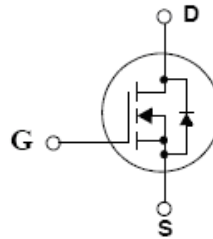
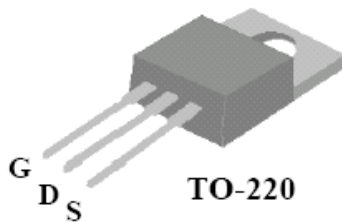
#### 1 Description

These N-Channel enhancement mode power field effect transistors are produced using planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

#### 2 Features

- 55V / 110A,  $R_{DS(on)} = 6m$  (typ) @  $V_{GS} = 10V, I_D = 66A$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability..



#### 3 Absolute Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted

Symbol	Parameter	apQ110SN5EA	Units
$V_{DSS}$	Drain-Source Voltage	55	V
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ C$ ) - Continuous ( $T_C = 100^\circ C$ )	110	A
		66	A
$I_{DM}$	Drain Current – Pulsed	400	A
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$E_{AS}$	Single Pulsed Avalanche Energy	264	mJ
$I_{AR}$	Avalanche Current	62	A
$E_{AR}$	Repetitive Avalanche Energy	20	mJ
dv/dt	Peak Diode Recovery dv/dt	5.0	V/ns
$P_D$	Power Dissipation ( $T_C = 25^\circ C$ ) - De-rate above $25^\circ C$	200	W
		1.3	W/ $^\circ C$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ C$
$T_L$	soldering temperature for 10 seconds	300	$^\circ C$

\* note :

Repetitive Rating: Pulse width limited by maximum junction temperature.

$V_{DD} = 30V$ , starting  $T_J = 25^\circ C$ ,  $L = TBD$ ,  $R_G = 0\Omega$ ,  $I_{AS} = 110A$

$I_{SD} \leq 110A$ ,  $di/dt \leq 100A/\mu s$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 150^\circ C$



# DEVICE SPECIFICATION

apQ110SN5EA

55V/110A N-Channel MOSFET

## 4 Thermal Characteristics

Symbol	Parameter	apQ110SN5EA		Units
		Typ.	Max.	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	--	0.75	$^{\circ}\text{C}/\text{W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.50	--	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	--	62	$^{\circ}\text{C}/\text{W}$

## 5 Electrical Characteristics $T_C = 25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	55	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 1\text{ mA}$ , Referenced to $25^{\circ}\text{C}$	--	0.058	--	$\text{V}/^{\circ}\text{C}$
$I_{DSS}$	Gate to Source leakage current	$V_{DS} = 55\text{ V}, V_{GS} = 0\text{ V}$	--	--	25	$\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage Current, Forward	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
$I_{GSSR}$	Gate-Body Leakage Current, Reverse	$V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 66\text{ A}$	--	6	8	m $\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 10\text{ V}, I_D = 55\text{ A}$	--	--	100	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 27.5\text{ V}, V_{GS} = 0\text{ V}, f = 1.0\text{ MHz}$	--	5340	--	pF
$C_{oss}$	Output Capacitance		--	1006	--	pF
$C_{rss}$	Reverse Transfer Capacitance		--	49.8	--	pF
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 27.5\text{ V}, I_D = 66\text{ A}, R_G = 25\ \Omega, R_D = 0.42$ $V_{GS} = 10\text{ V}$	--	18.4	--	ns
$t_r$	Turn-On Rise Time		--	56.3	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	58.3	--	ns
$t_f$	Turn-Off Fall Time		--	22.8	--	ns
$Q_g$	Total Gate Charge	$V_{DS} = 27.5\text{ V}, I_D = 66\text{ A}, V_{GS} = 10\text{ V}$	--	79.2	--	nC
$Q_{gs}$	Gate-Source Charge		--	20.1	--	nC
$Q_{gd}$	Gate-Drain Charge		--	23.0	--	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current		--	--	110	A
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current		--	--	440	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 55\text{ A}$	--	--	1.5	V



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$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0\text{ V}, I_F = 66\text{ A},$ $di_F/dt = 100\text{ A}/\mu\text{s}$	--	70	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	120	--	$\mu\text{C}$

**Notes:**

Repetitive Rating: Pulse width limited by maximum junction temperature.  
 $V_{DD} = 30\text{V}$ , starting  $T_J = 25^\circ\text{C}$ ,  $L = \text{TBD}$ ,  $R_G = 0\Omega$ ,  $I_{AS} = 110\text{A}$   
 $I_{SD} \leq 110\text{A}$ ,  $di/dt \leq 100\text{A}/\mu\text{s}$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 150^\circ\text{C}$   
Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ . Depend on FT Test.  
CP Test



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55V/110A N-Channel MOSFET

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### Note

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