

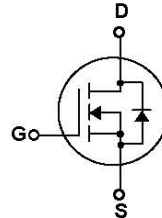
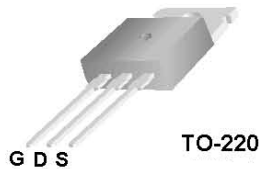
#### 1 Description

These N-Channel enhancement mode power field effect transistors are produced using planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

#### 2 Features

- 400V / 5.5A ,  $R_{DS(on)} = 0.85\Omega(\text{typ}) @ V_{GS} = 10\text{V}, I_D = 3.3\text{A}$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability..



#### 3 Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	apQ5ESN40A	Units
		TO-220	
$V_{DSS}$	Drain-Source Voltage	400	V
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ ) - Continuous ( $T_C = 100^\circ\text{C}$ )	5.5	A
		3.3	A
$I_{DM}$	Drain Current - Pulsed	22	A
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy	290	mJ
$I_{AR}$	Avalanche Current	5.5	A
$E_{AR}$	Repetitive Avalanche Energy	7.4	mJ
dv/dt	Peak Diode Recovery dv/dt	4.0	V/ns
$P_D$	Power Dissipation ( $T_C = 25^\circ\text{C}$ ) - De-rate above $25^\circ\text{C}$	73	W
		0.58	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

\* note :

Repetitive Rating: Pulse width limited by maximum junction temperature.

$V_{DD} = 50\text{V}$ , starting  $T_J = 25^\circ\text{C}$ ,  $L = 16\text{mH}$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 5.5\text{A}$

$I_{SD} \leq 5.5\text{A}$ ,  $di/dt \leq 90\text{A}/\mu\text{s}$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 150^\circ\text{C}$



# DEVICE SPECIFICATION

apQ5ESN40A

400V/5.5A N-Channel MOSFET

## 4 Thermal Characteristics

Symbol	Parameter	apQ5ESN40A		Units
		TO-220		
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.71		$^{\circ}\text{C}/\text{W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5		$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.		$^{\circ}\text{C}/\text{W}$

## 5 Electrical Characteristics $T_C = 25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	400	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\ \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$	--	0.54	--	$\text{V}/^{\circ}\text{C}$
$I_{DSS}$	Gate to Source leakage current	$V_{DS} = 400\text{ V}, V_{GS} = 0\text{ V}$	--	--	20	$\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
$I_{GSSR}$	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$ 4	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 3.3\text{ A}$	--	0.85	1	$\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 10\text{ V}, I_D = 3.3\text{ A}$	2.9	4.4	--	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V}, f = 1.0\text{ MHz}$	--	700	--	pF
$C_{oss}$	Output Capacitance		--	170	--	pF
$C_{riss}$	Reverse Transfer Capacitance		--	64	--	pF
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 200\text{ V}, I_D = 3.5\text{ A}, R_D = 57\ \Omega, R_G = 12$	--	10	-	ns
$t_r$	Turn-On Rise Time		--	15	-	ns
$t_{d(off)}$	Turn-Off Delay Time		--	38	-	ns
$t_f$	Turn-Off Fall Time		--	14	-	ns
$Q_g$	Total Gate Charge	$V_{DS} = 320\text{ V}, I_D = 3.5\text{ A}, V_{GS} = 10\text{ V}$	--	--	38	nC
$Q_{gs}$	Gate-Source Charge		--	--	5.7	nC
$Q_{gd}$	Gate-Drain Charge		--	--	22	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current		--	--	5.5	A
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current		--	--	22	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 2.75\text{ A}$	--	--	1.5	V



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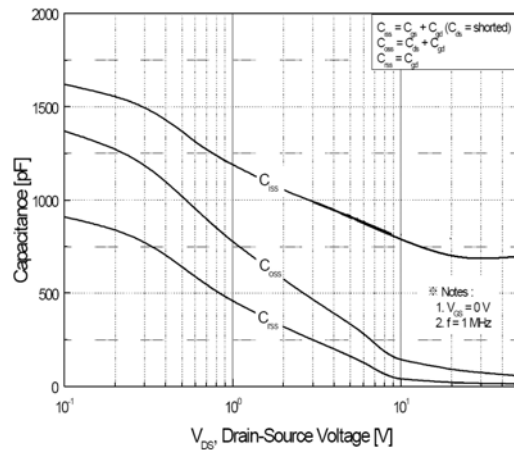
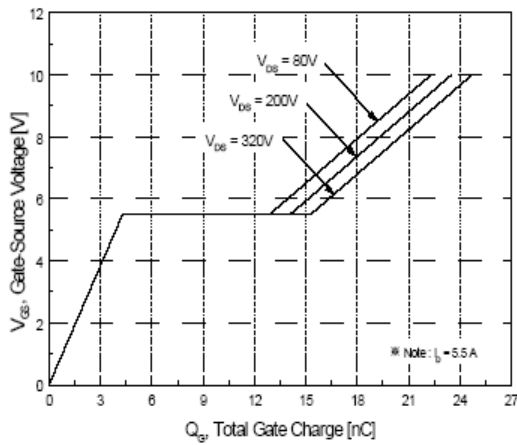
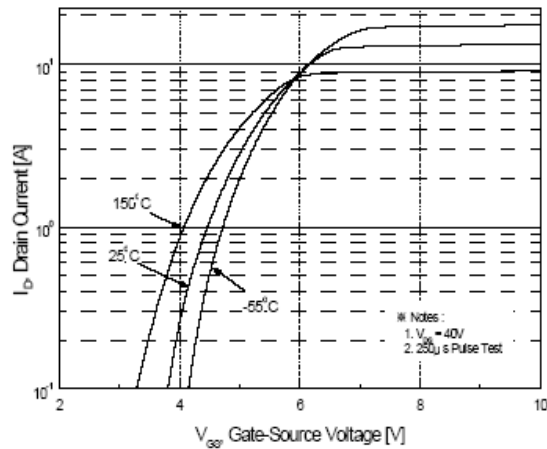
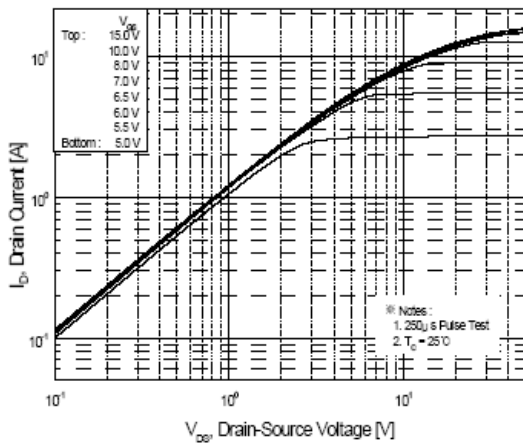
apQ5ESN40A

## 400V/5.5A N-Channel MOSFET

$t_{rr}$	Reverse Recovery Time	$V_{GS}=0V, I_S=3.5A,$ $di/dt=100A/\mu s$	--	--	1.6	ns
$Q_{rr}$	Reverse Recovery Charge		--	270	530	$\mu C$

**Notes:**

Repetitive Rating: Pulse width limited by maximum junction temperature.  
 $V_{DD}=50V$ , starting  $T_J=25^\circ C$ ,  $L=16mH$ ,  $R_G=25\Omega$ ,  $I_{AS}=5.5A$   
 $I_{SD} \leq 5$ ,  $di/dt \leq 100A/\mu s$ ,  $V_{DD} \leq V(BR)DSS$ ,  $T_J \leq 150$   
 Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ . Depend on FT Test.

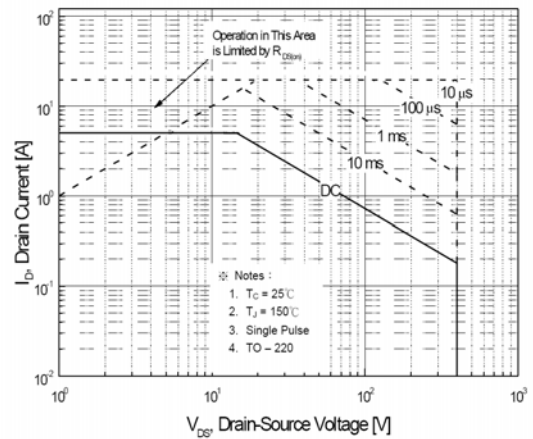
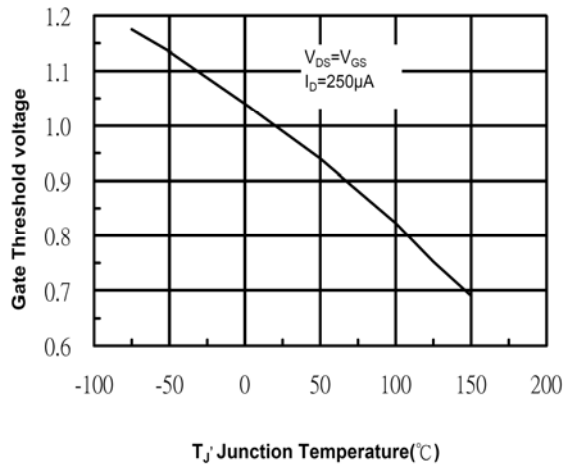
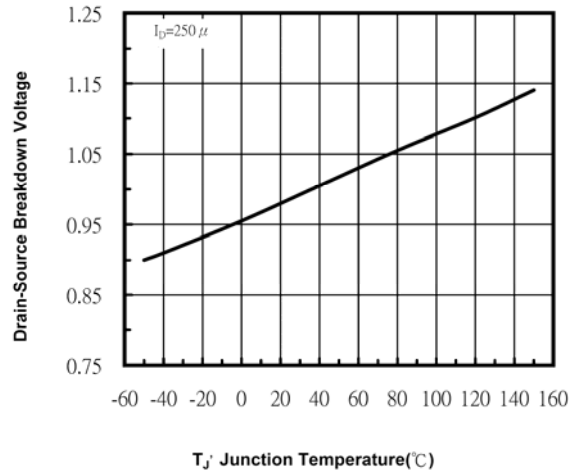
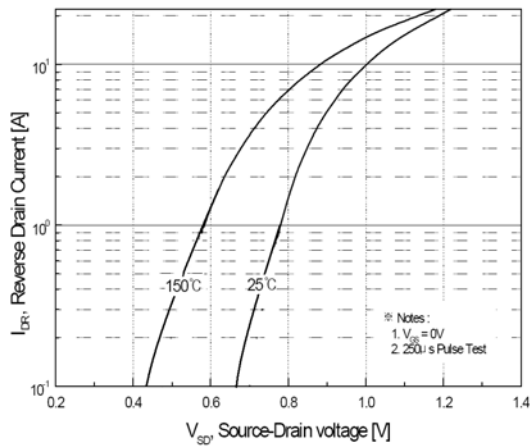
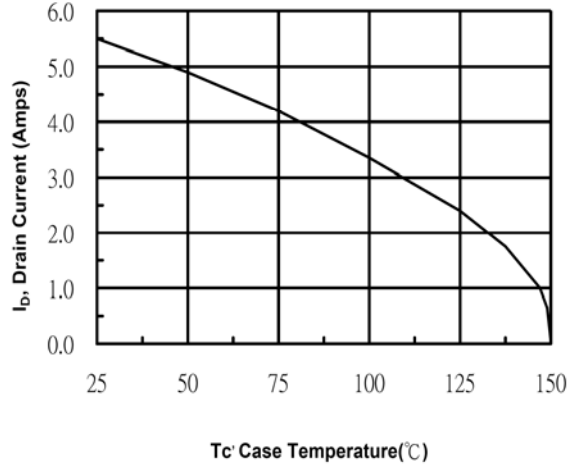
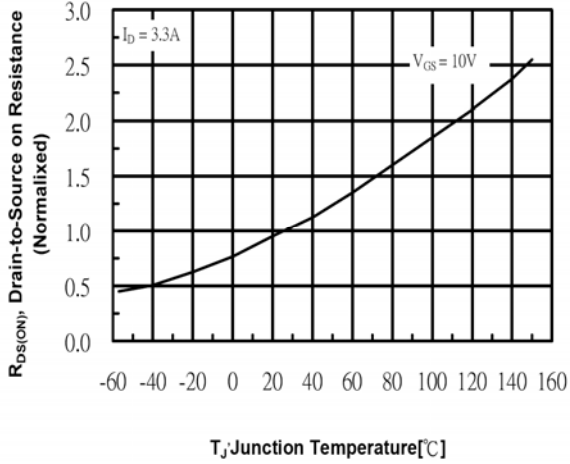


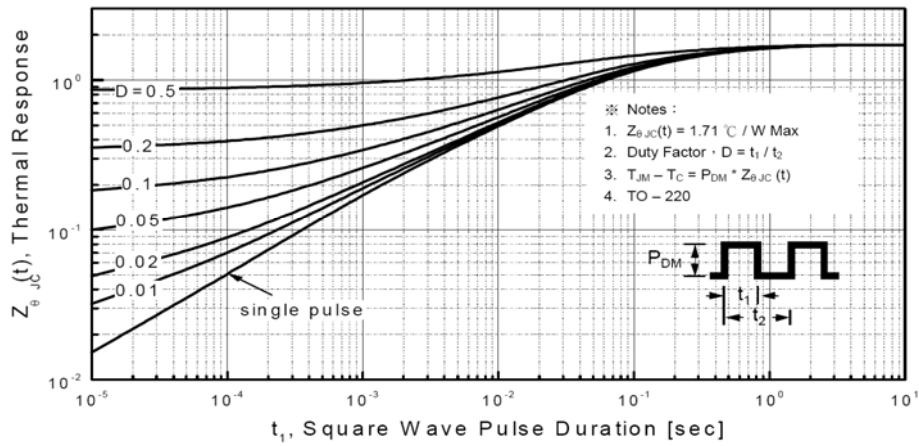


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400V/5.5A N-Channel MOSFET

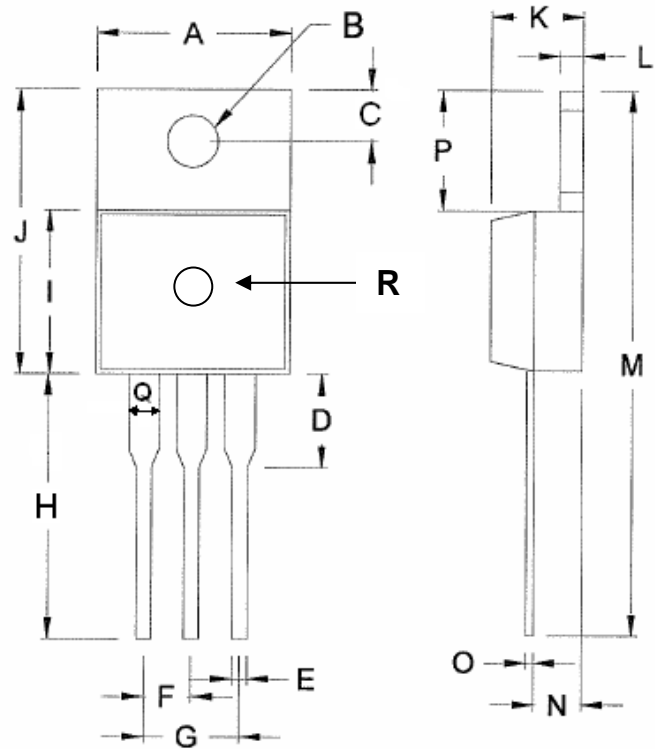




#### 6 Package Dimensions

#### TO-220

TO-220 DIMENSION			
DIM	MILLIMETERS		
	MIN	MAX	TYP.
A	10.04	10.41	10.23
B	3.75	3.88	3.82
C	2.50	2.84	2.67
D	3.31	4.50	3.91
E	0.70	0.91	0.81
F	2.54(typ.)		2.54
G	5.08(typ.)		5.08
H	13.47	14.20	13.84
I	8.50	9.00	8.80
J	14.80	15.49	15.15
K	4.32	4.57	4.45
L	1.22	1.37	1.30
M	28.27	29.69	28.98
N	2.40	2.90	2.65
O	0.36	0.53	0.45
P	5.97	6.47	6.22
Q	1.15	1.45	1.30
R	2.0(typ.)		2.00





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400V/5.5A N-Channel MOSFET

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### Note

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