

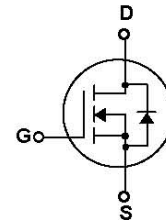
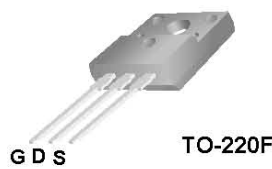
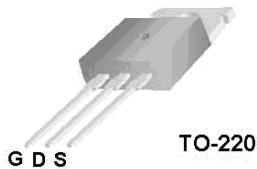
1 Description

These N-Channel enhancement mode power field effect transistors are produced using planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

2 Features

- 200V / 9.5A
- $R_{DS(on)} = 0.34\Omega(\text{typ})$ $V_{GS} = 10V$, $I_D = 4.75A$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability..



3 Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	apQ9ESN20AB	Units
		TO-252	
V_{DSS}	Drain-Source Voltage	200	V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$)	9.5	A
I_{DM}	Drain Current – Pulsed ①	38	A
V_{GS}	Gate-Source Voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy ②	210	mJ
I_{AR}	Avalanche Current	9.5	A
E_{AR}	Repetitive Avalanche Energy	7.2	mJ
dv/dt	Peak Diode Recovery dv/dt ③	5.5	V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$) -De-rate above 25°C	72	W
		0.57	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

* note :

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② starting $T_J = 25^\circ\text{C}$, $L = \text{TBDmH}$, $R_G = 25\Omega$, $I_{AS} = 9.5A$
- ③ $I_{SD} \leq 9.5A$, $di/dt \leq 300A/\mu\text{s}$, $V_{DD} \leq V_{(BR)DSS}$, $T_J \leq 150^\circ\text{C}$.



DEVICE SPECIFICATION

apQ9ESN20AB

200V/9.5A N-Channel MOSFET

4 Thermal Characteristics

Symbol	Parameter	apQ9ESN20AB	
		TO-252	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.74	$^{\circ}\text{C}/\text{W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	$^{\circ}\text{C}/\text{W}$

5 Electrical Characteristics $T_C = 25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	200	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\ \mu\text{A}$, Referenced to 25°C	--	0.25	--	$\text{V}/^{\circ}\text{C}$
I_{DSS}	Gate to Source leakage current	$V_{DS} = 200\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 4.75\text{ A}$ ④	--	0.34	0.4	Ω
g_{FS}	Forward Transconductance	$V_{DS} = 40\text{ V}, I_D = 4.75\text{ A}$ ①	--	5.5	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V}, f = 1.0\text{ MHz}$	--	400	--	pF
C_{oss}	Output Capacitance		--	100	--	pF
C_{rss}	Reverse Transfer Capacitance		--	40	--	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 300\text{ V}, I_D = 9.5\text{ A}, R_G = 10\ \Omega,$ ④	--	10	--	ns
t_r	Turn-On Rise Time		--	90	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	70	--	ns
t_f	Turn-Off Fall Time		--	70	--	ns
Q_g	Total Gate Charge	$V_{DS} = 160\text{ V}, I_D = 9.5\text{ A}, V_{GS} = 10\text{ V}$ ④	--	20	--	nC
Q_{gs}	Gate-Source Charge		--	3.0	--	nC
Q_{gd}	Gate-Drain Charge		--	10	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	9.5	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	38	A
V_{SD}	Drain-Source Diode	$V_{GS} = 0\text{ V}, I_S = 9.5\text{ A}$	--	--	1.5	V



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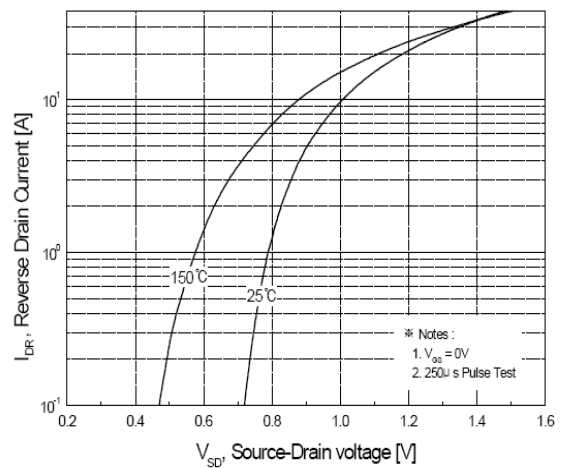
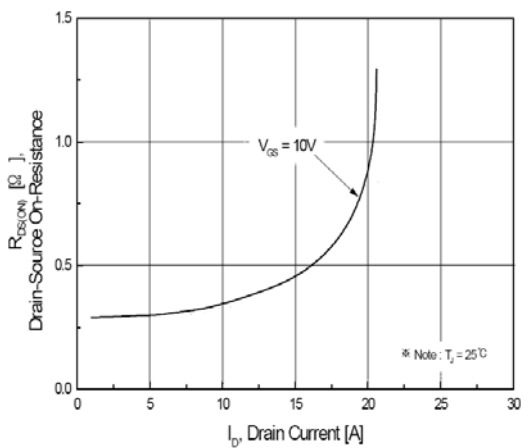
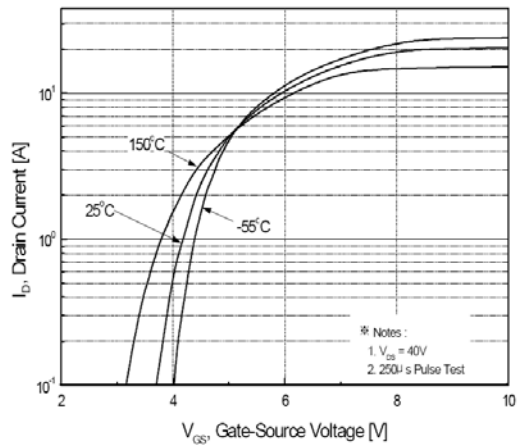
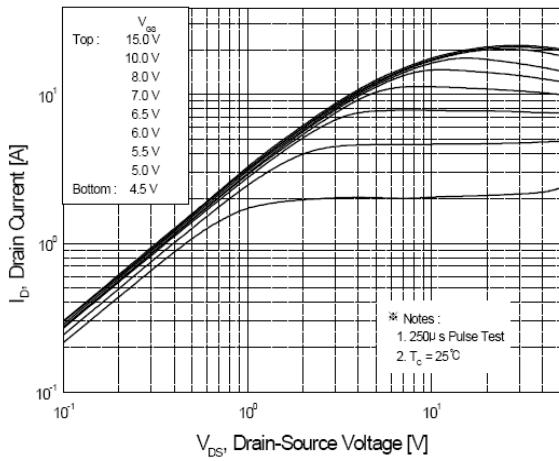
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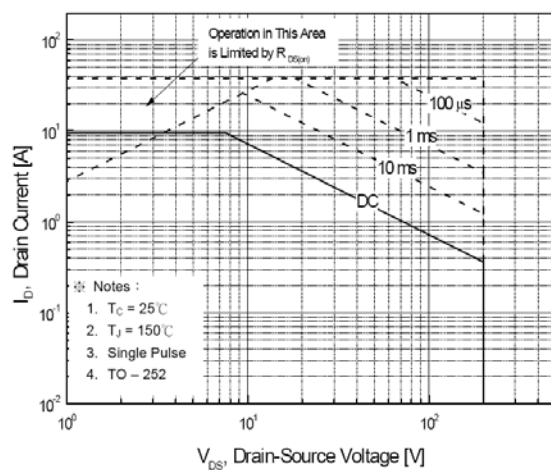
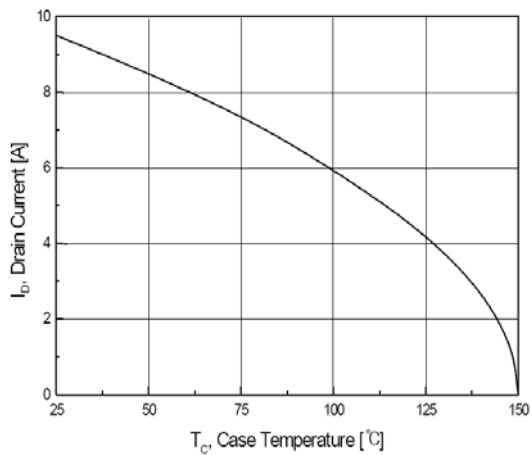
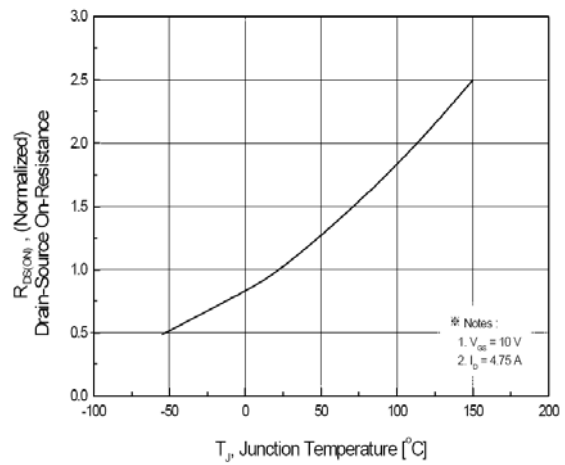
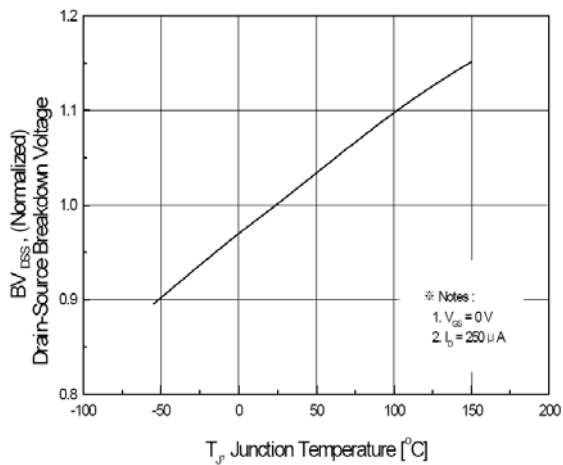
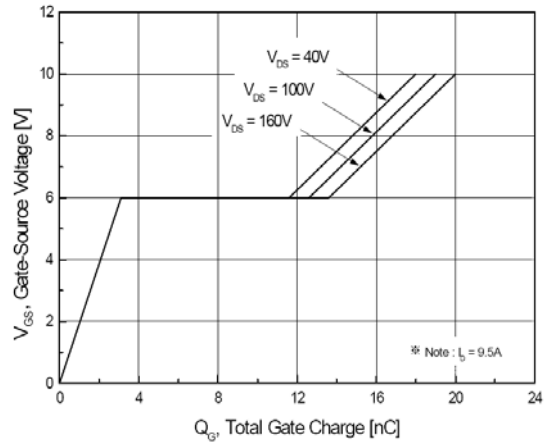
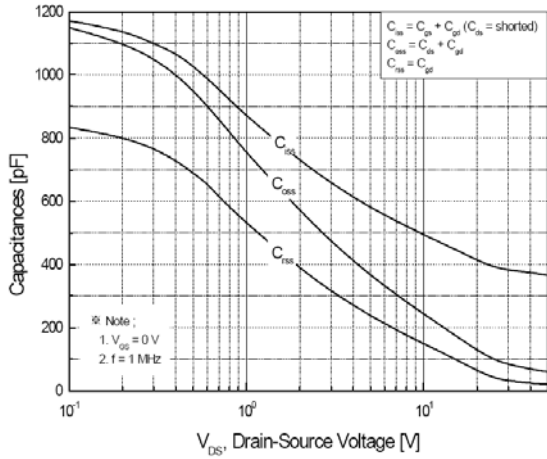
200V/9.5A N-Channel MOSFET

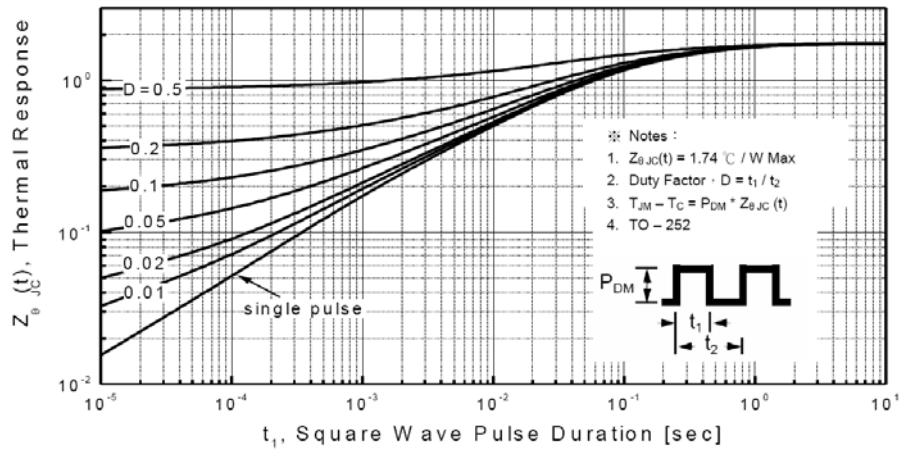
	Forward Voltage					
t_{rr}	Reverse Recovery Time	$V_{GS} = 0\text{ V}, I_F = 9.5\text{ A},$ $dI_F/dt = 100\text{ A}/\mu\text{s}$ ④	--	160	--	ns
Q_{rr}	Reverse Recovery Charge		--	1.0	--	μC

Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② starting $T_J = 25^\circ\text{C}$, $L = \text{TBD}$, $R_G = 25\Omega$, $I_{AS} = 9.5\text{A}$
- ③ $I_{SD} \leq 9.5\text{A}$, $di/dt \leq 100\text{A}/\mu\text{s}$, $V_{DD} \leq V_{(BR)DSS}$, $T_J \leq 150^\circ\text{C}$
- ④ Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

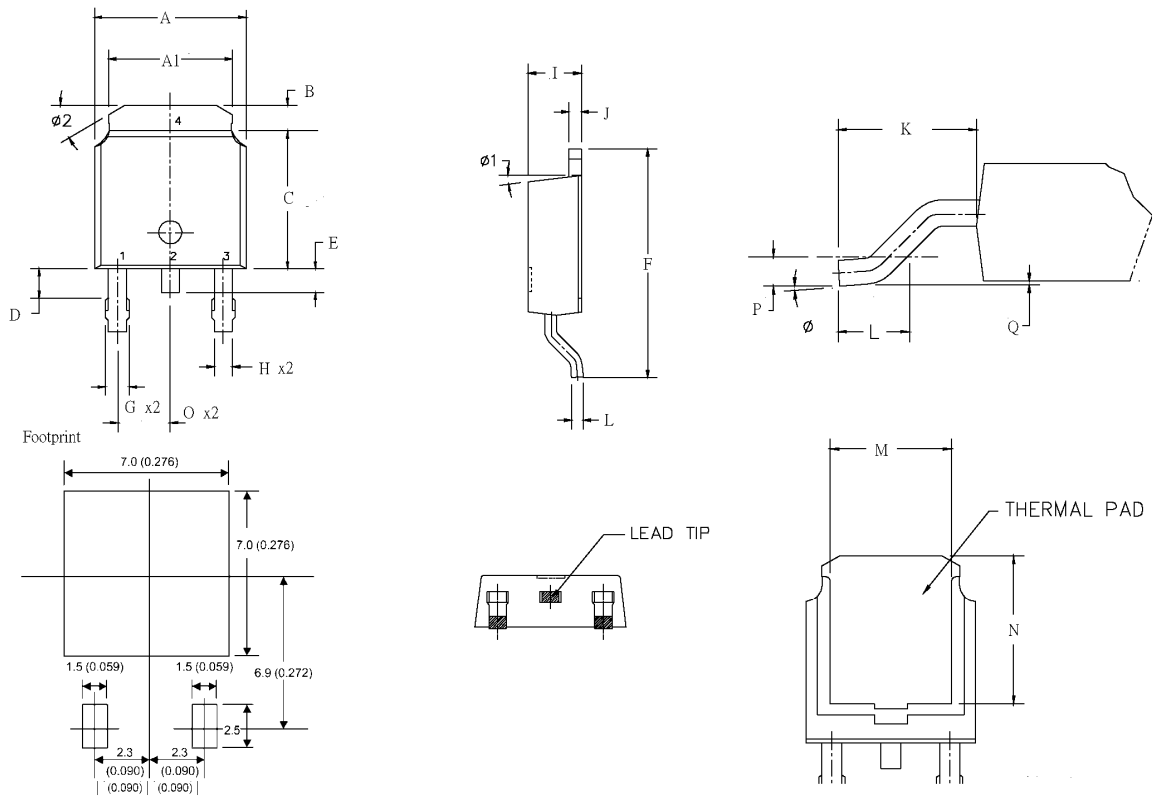






6 Package Dimensions

TO-252



TO-252 DIMENSION							
DIM	MILLIMETERS						
	MIN	MAX	TYP.	DIM	MIN	MAX	TYP.
A	6.35	6.73	6.57	J	0.46	0.89	0.68
A1	4.95	5.46	5.21	K	0°	10°	5°
B	0.89	1.27	1.08	L	0.46	1.78	0.54
C	5.97	6.22	6.10	M	4.32	--	--
D	25°	35°	30°	N	5.21	--	--
E	0.64	1.02	0.83	O	2.29 BSC		
F	9.40	10.41	9.91	P	0.51 BSC		
G	0.76	1.14	0.95	Q	--	0.13	--
H	0.64	0.89	0.77	R	0°	10°	5°
I	2.18	2.39	2.29	S	2.29	2.79	2.54



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200V/9.5A N-Channel MOSFET

Note

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contact

alpha pacific Technologies Co., Ltd
3F-6, No.18, Lane 609, Sec.5
Chung Sin road, Shan Chang City
Taipei-Hsien, TAIWAN, R.O.C

tel +886-2-2999 5456
fax +886-2-2999 5270
internet www.aptw.com